

FIG. 1

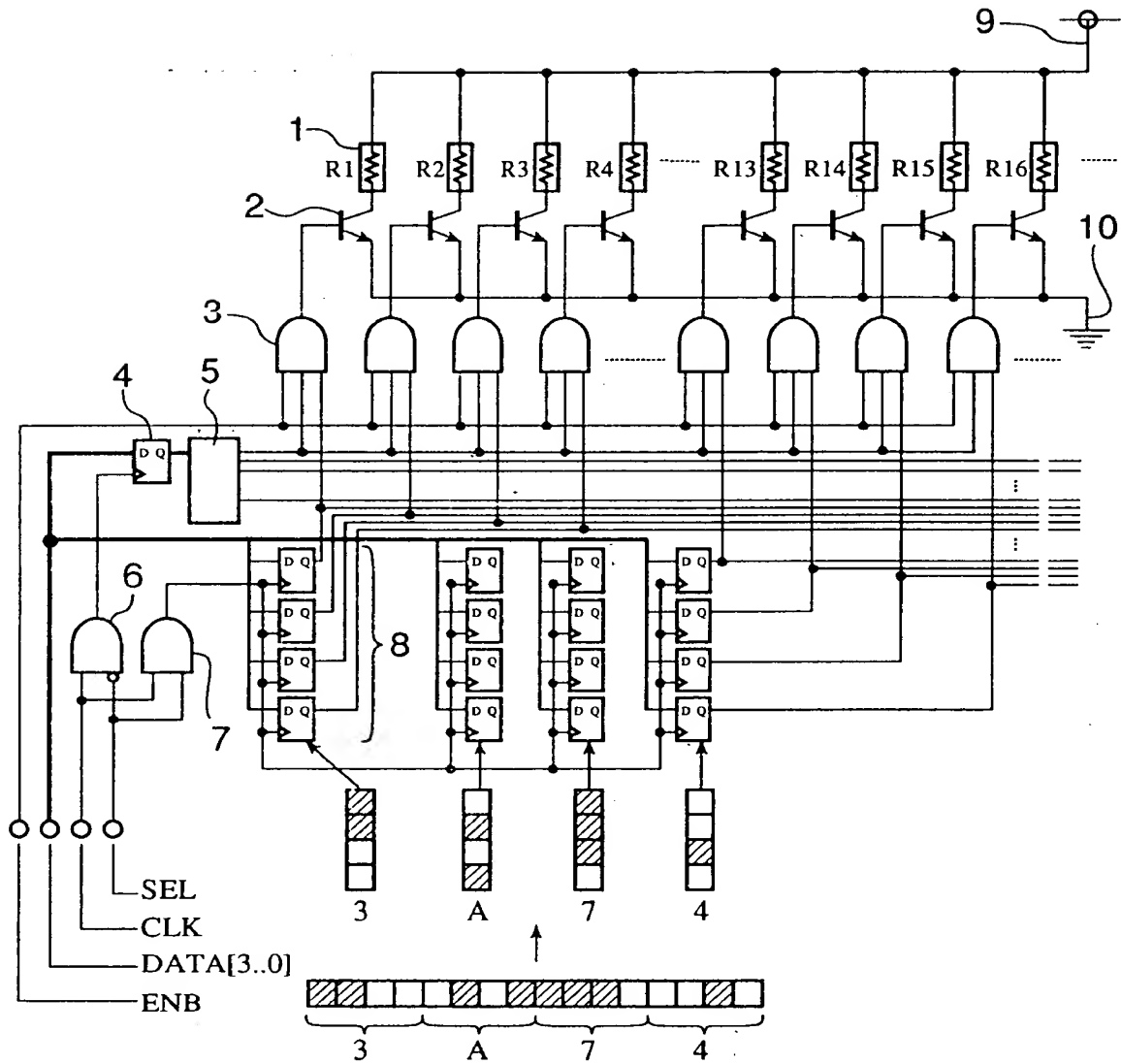
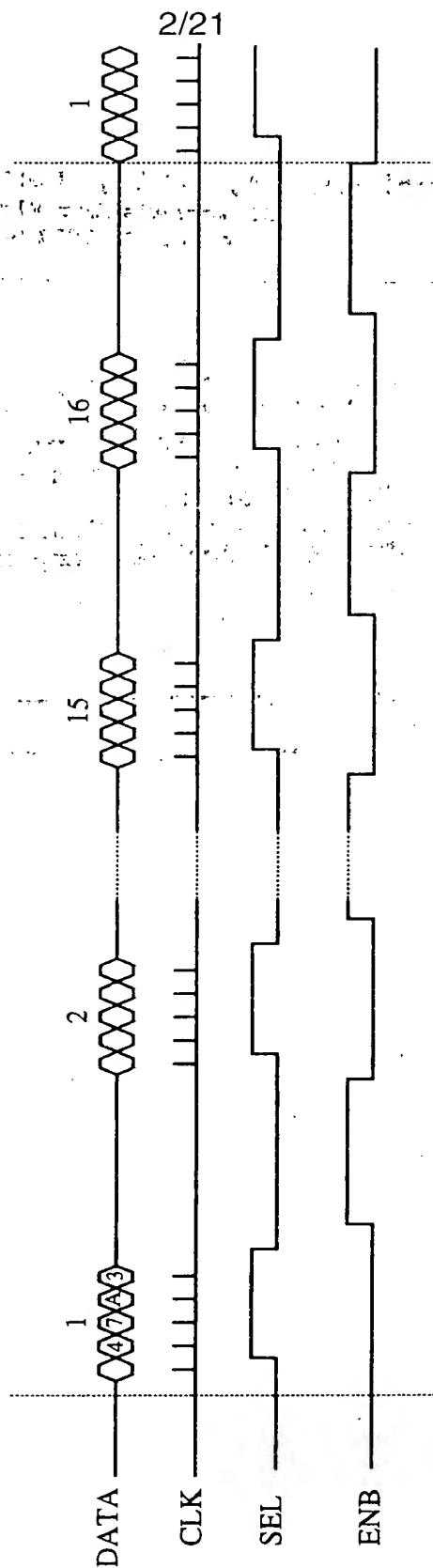


FIG. 2



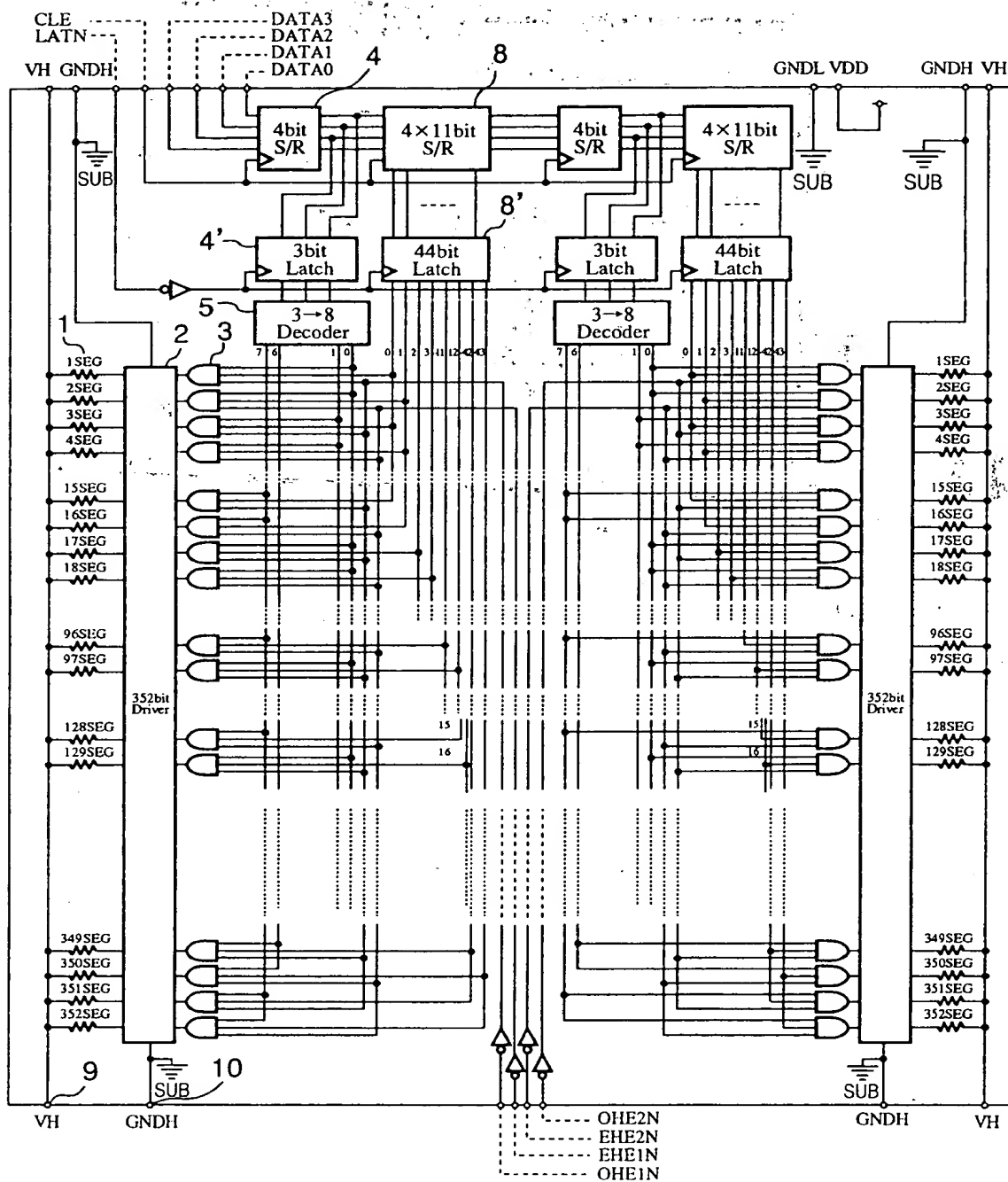


FIG. 4

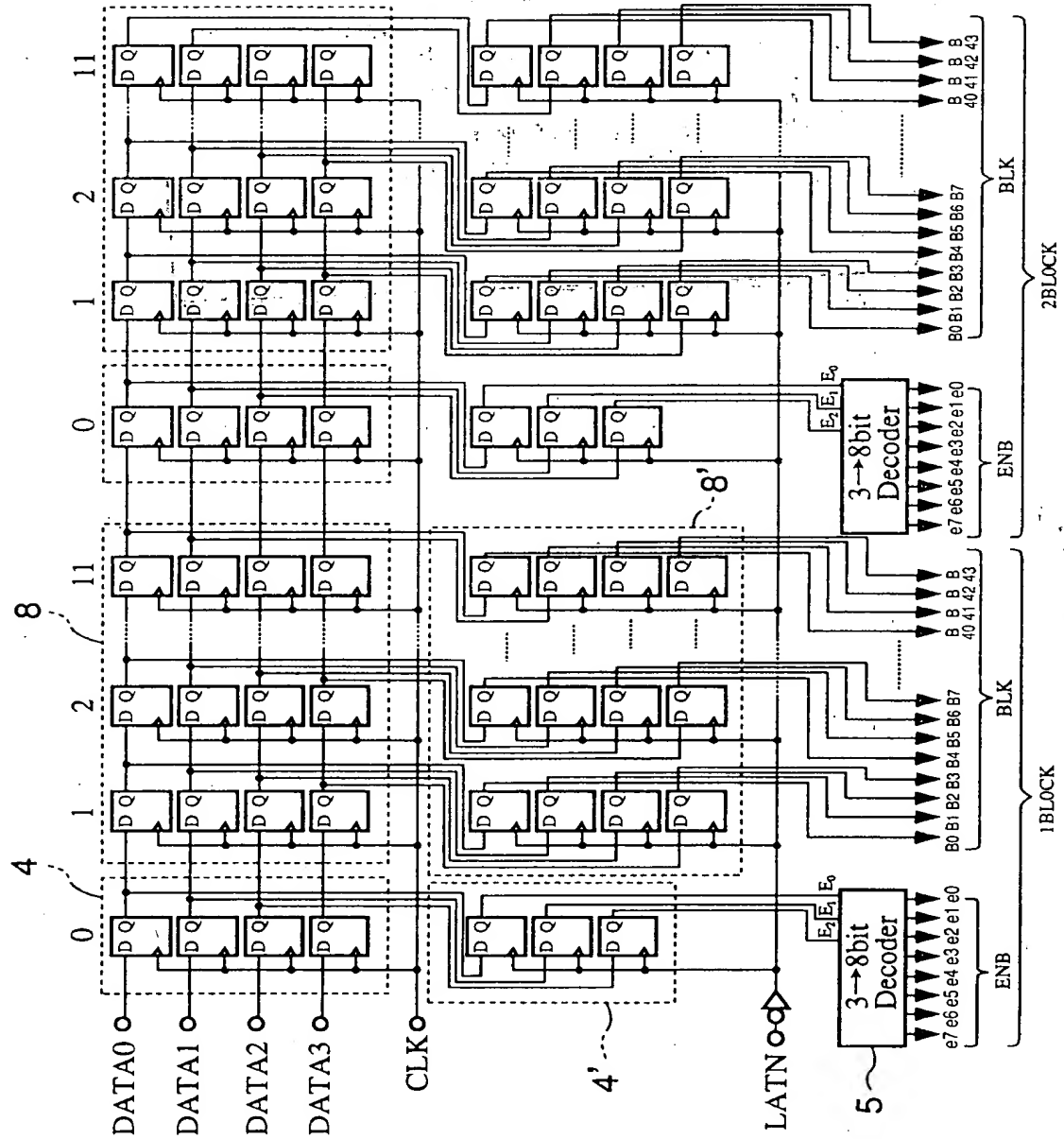


FIG. 5

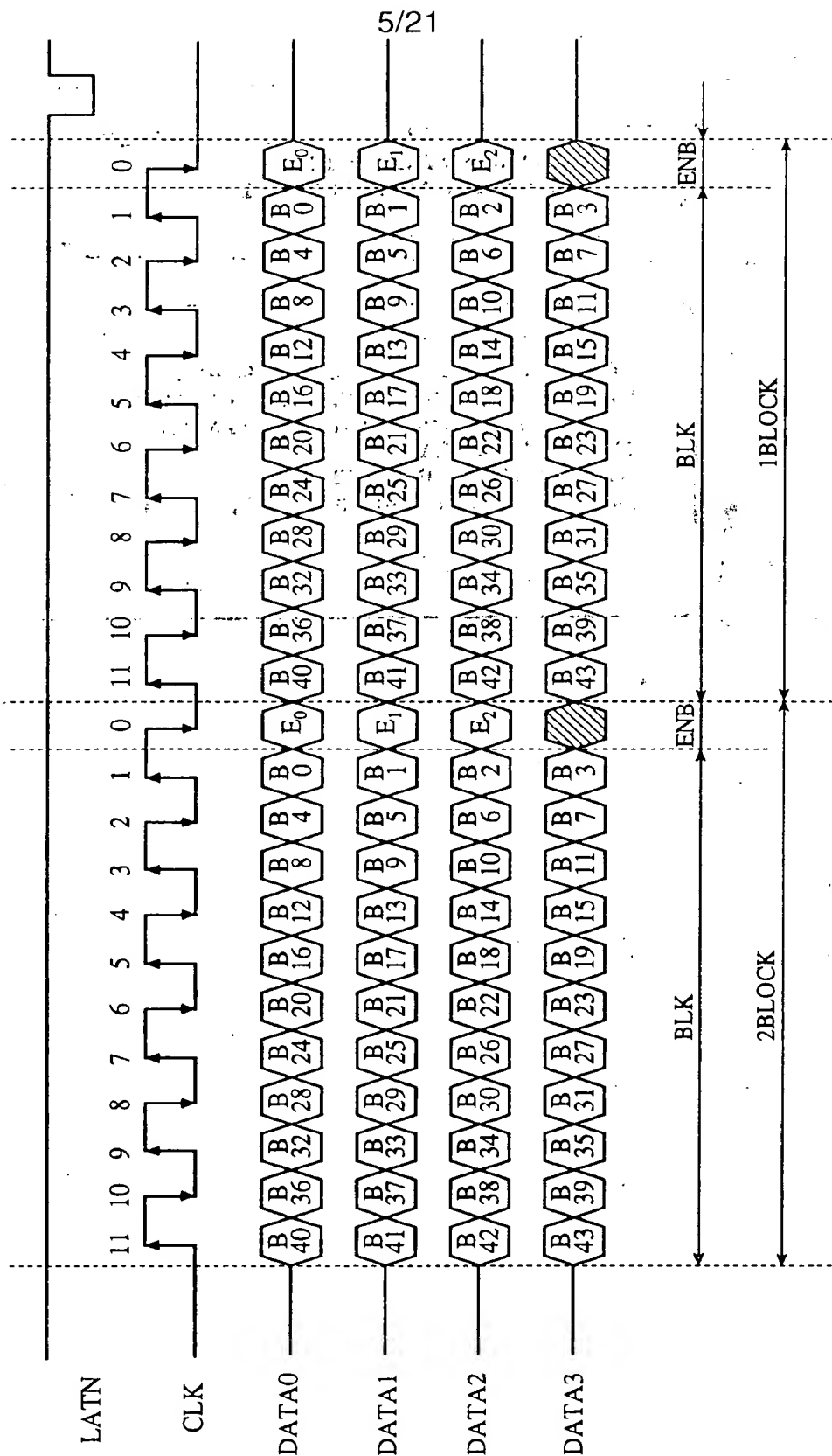
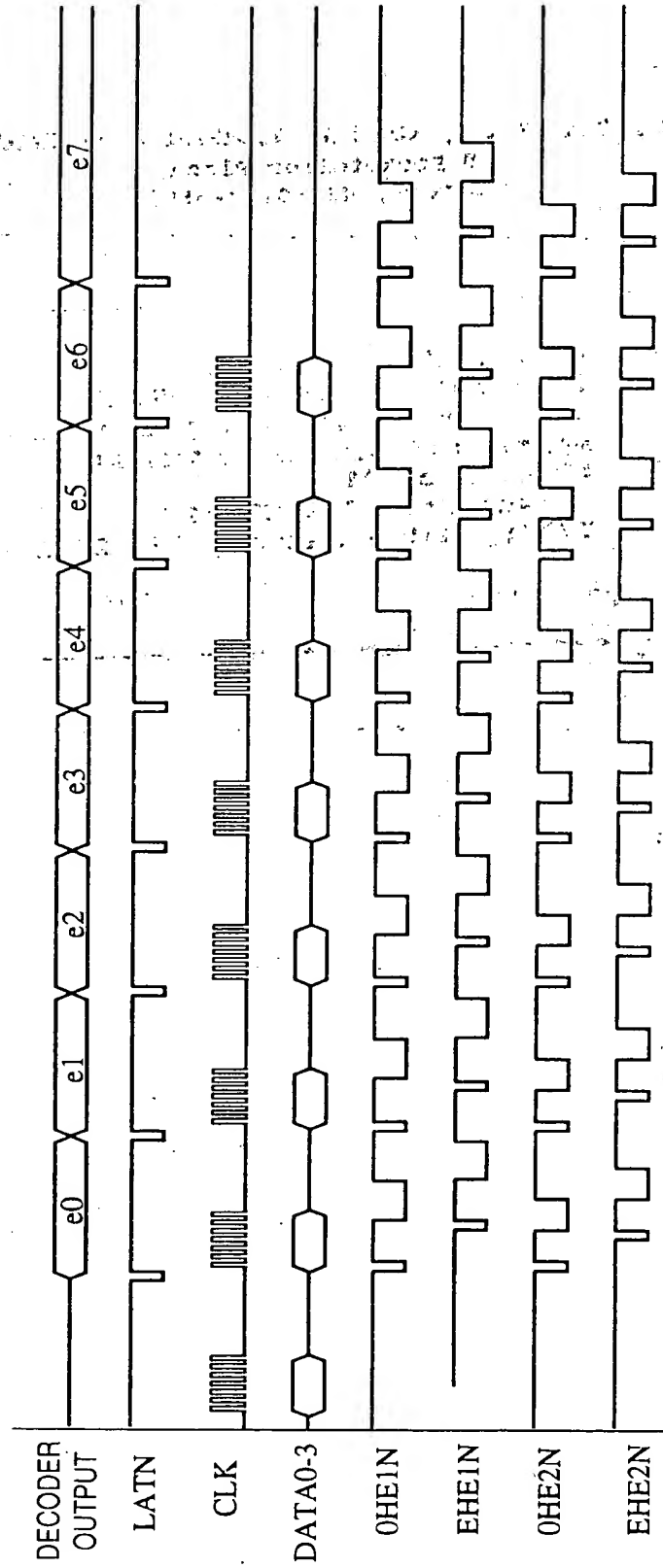
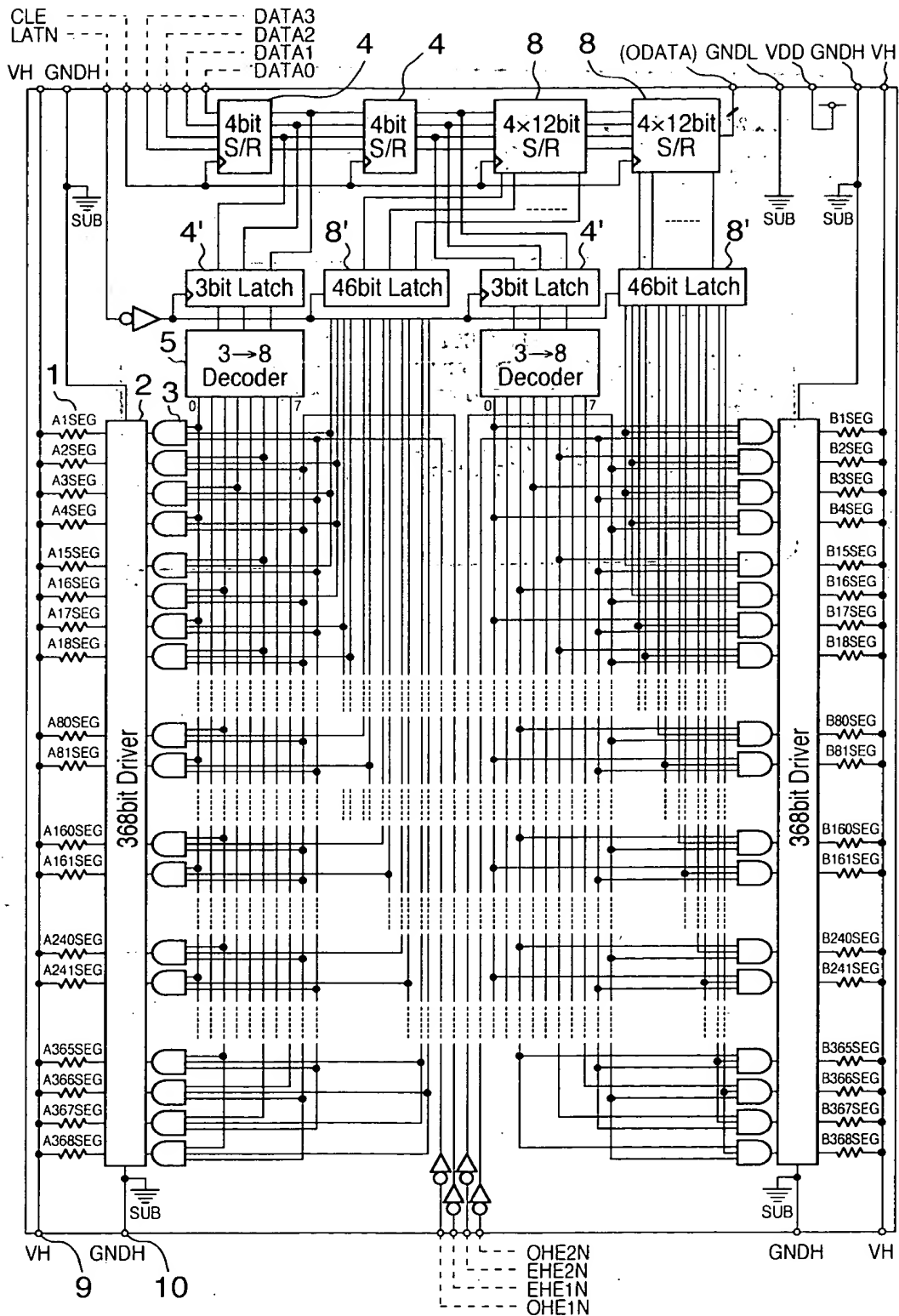


FIG. 6



7/21
FIG. 7



FOOTNOTES

FIG. 8

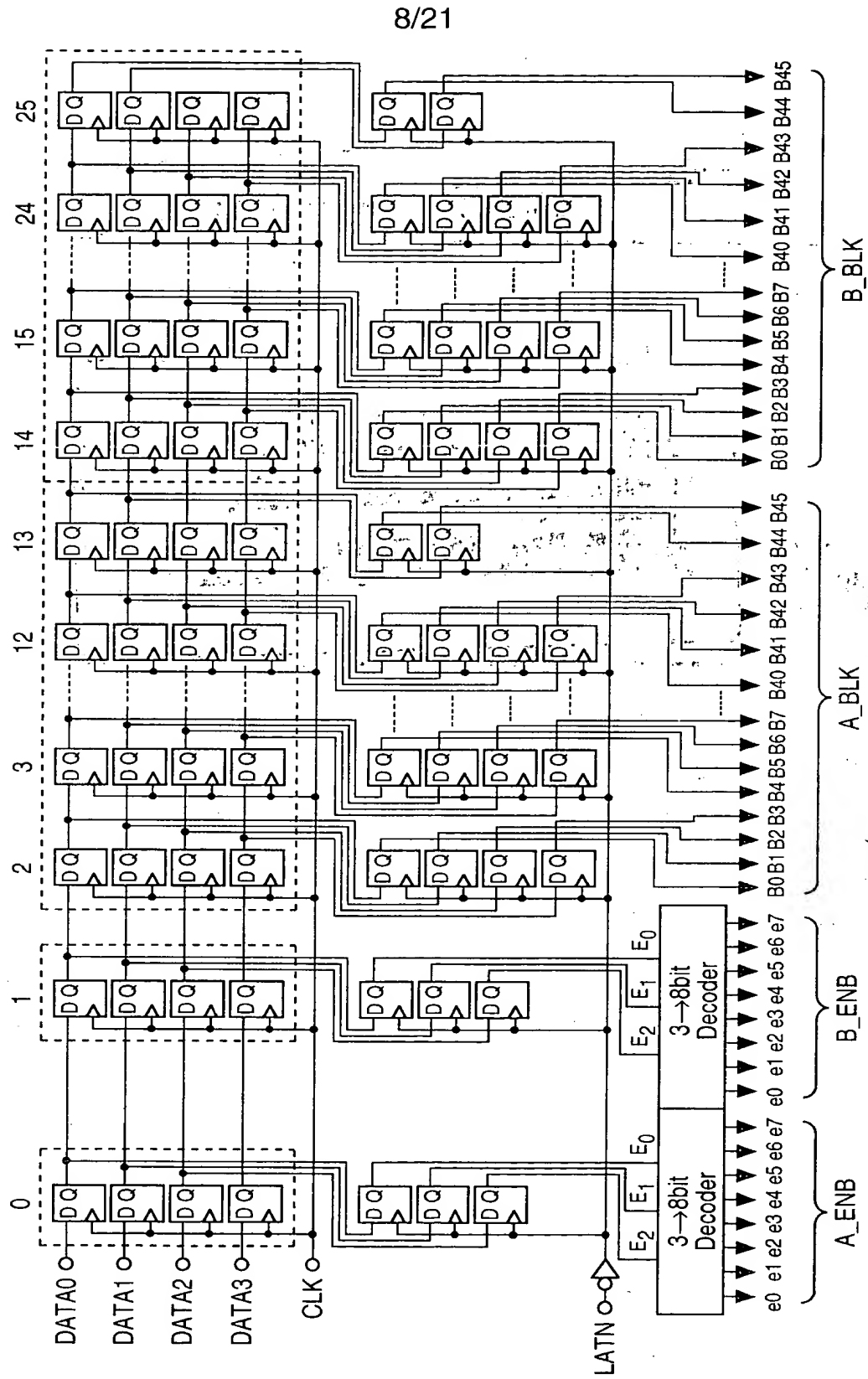


FIG. 9

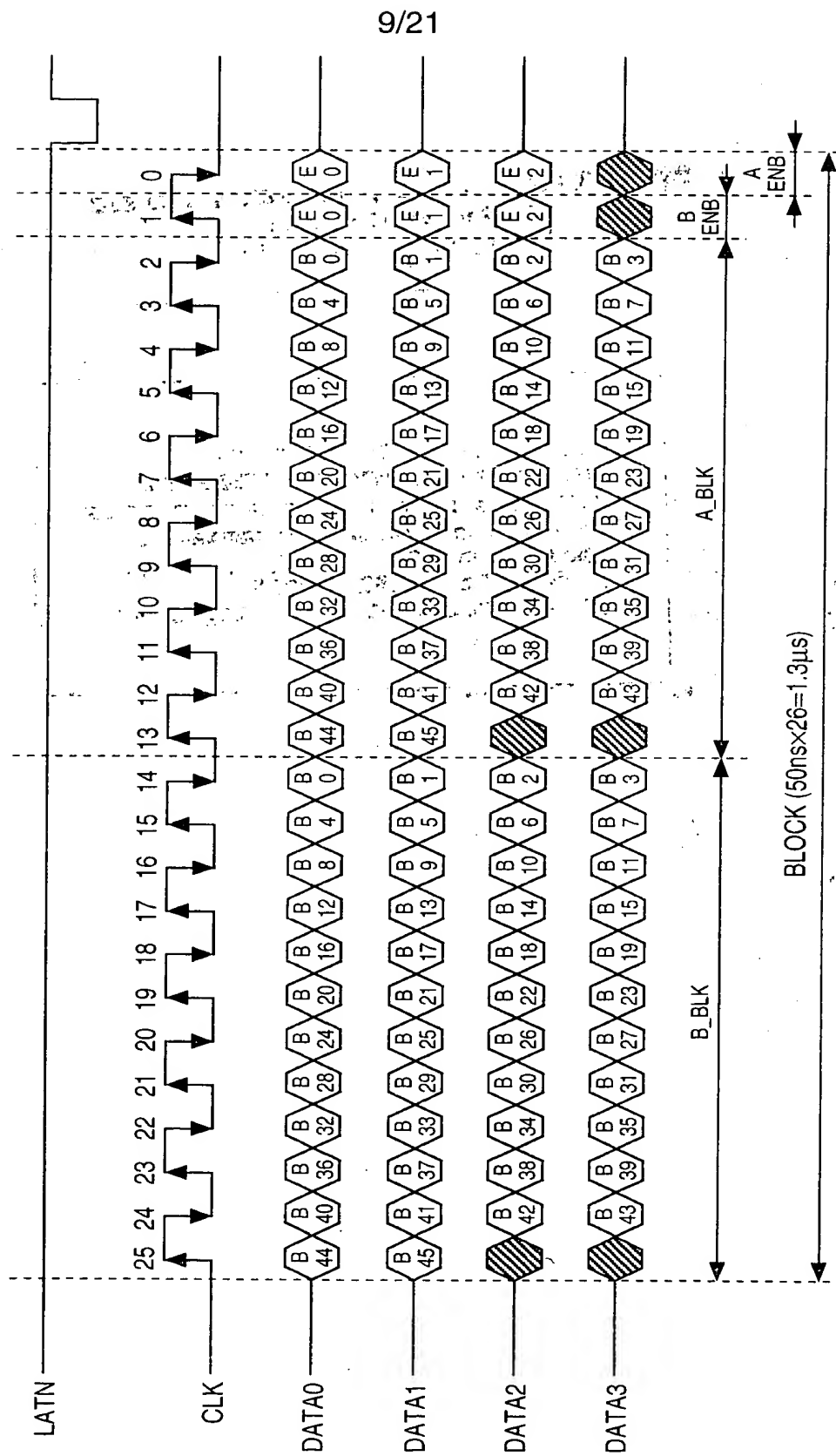


Figure 10 is a detailed schematic diagram of a digital circuit. The circuit includes a power supply (9) and ground (10). It features a series of resistors (R1, R2, R3, R4, R13, R14, R15, R16) and transistors (2) connected to a bus system (5). The bus system is connected to a series of AND gates (3). The outputs of the AND gates are connected to a series of multiplexers (4) and a final output block (8). The circuit is controlled by CLK, DATA, and ENB signals.

FIG. 11

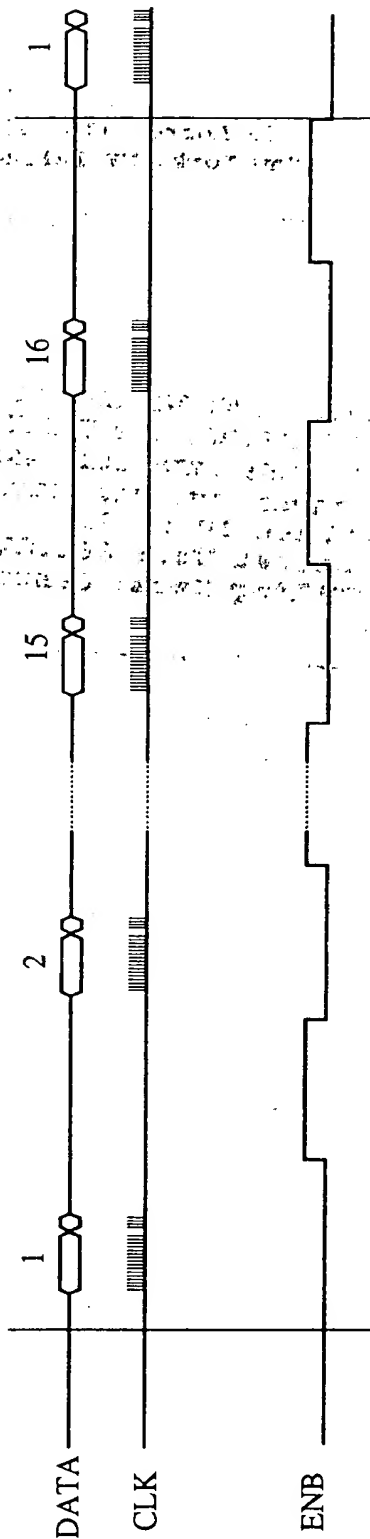


FIG. 12

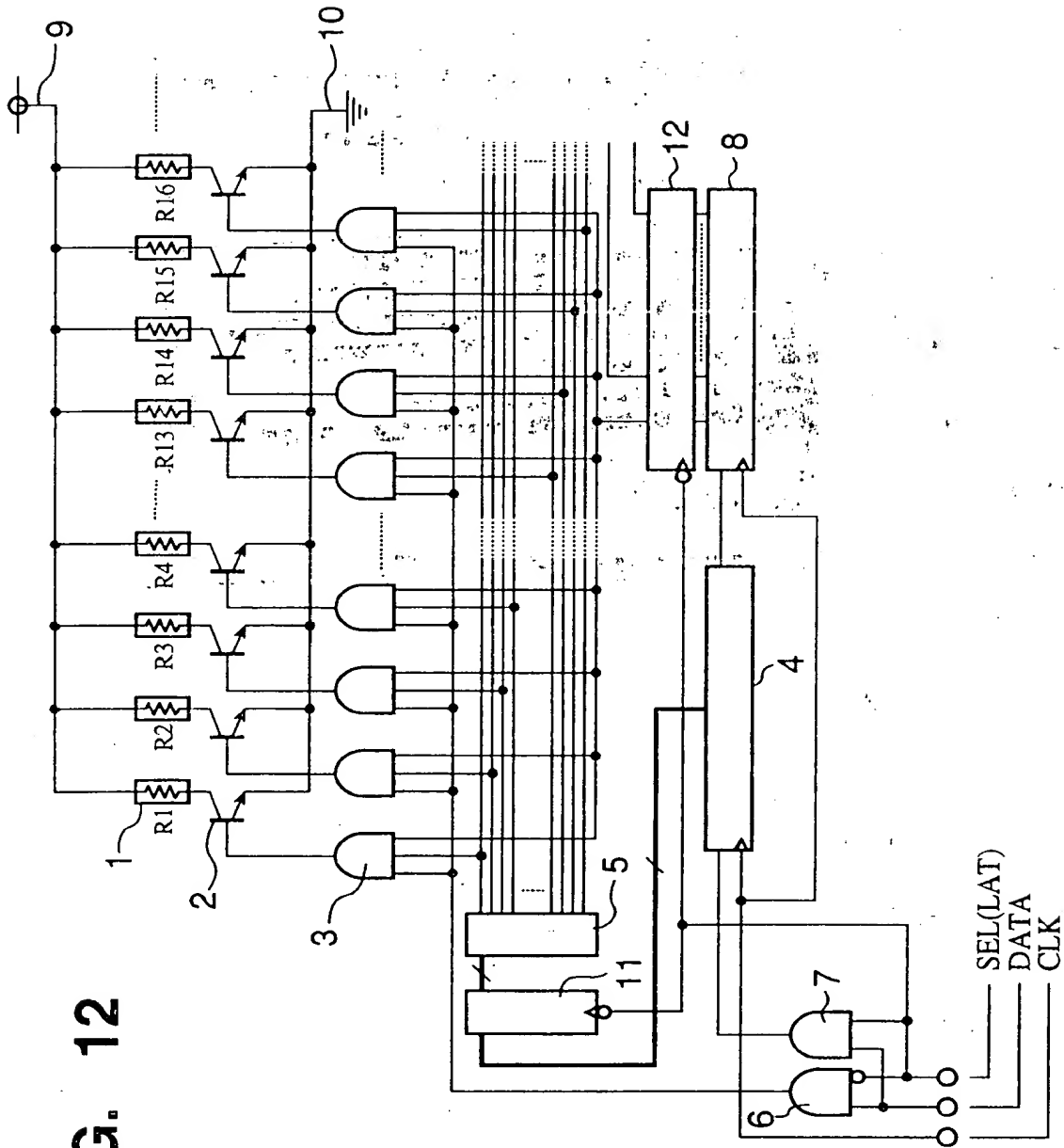


FIG. 13

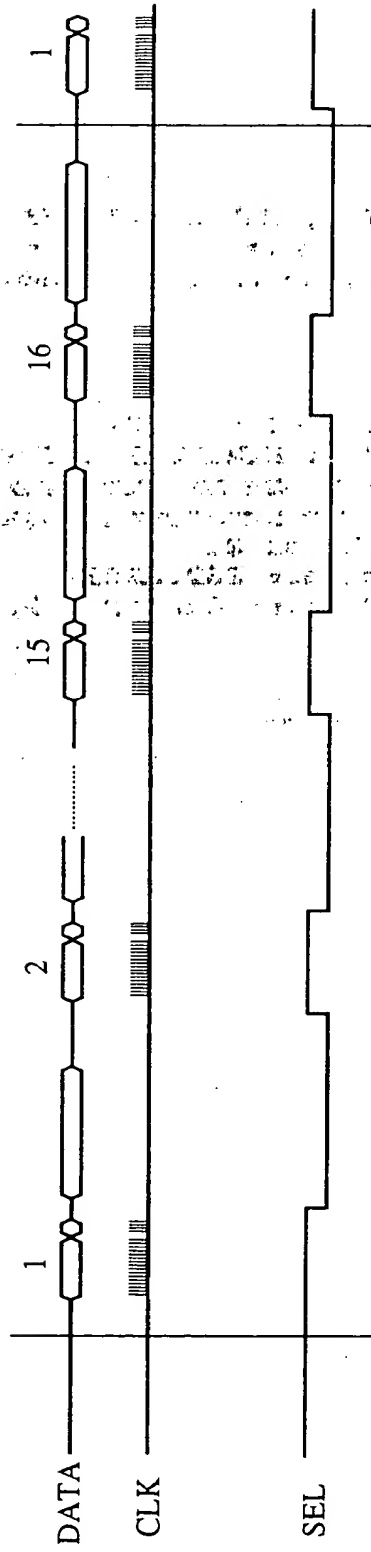


FIG. 14

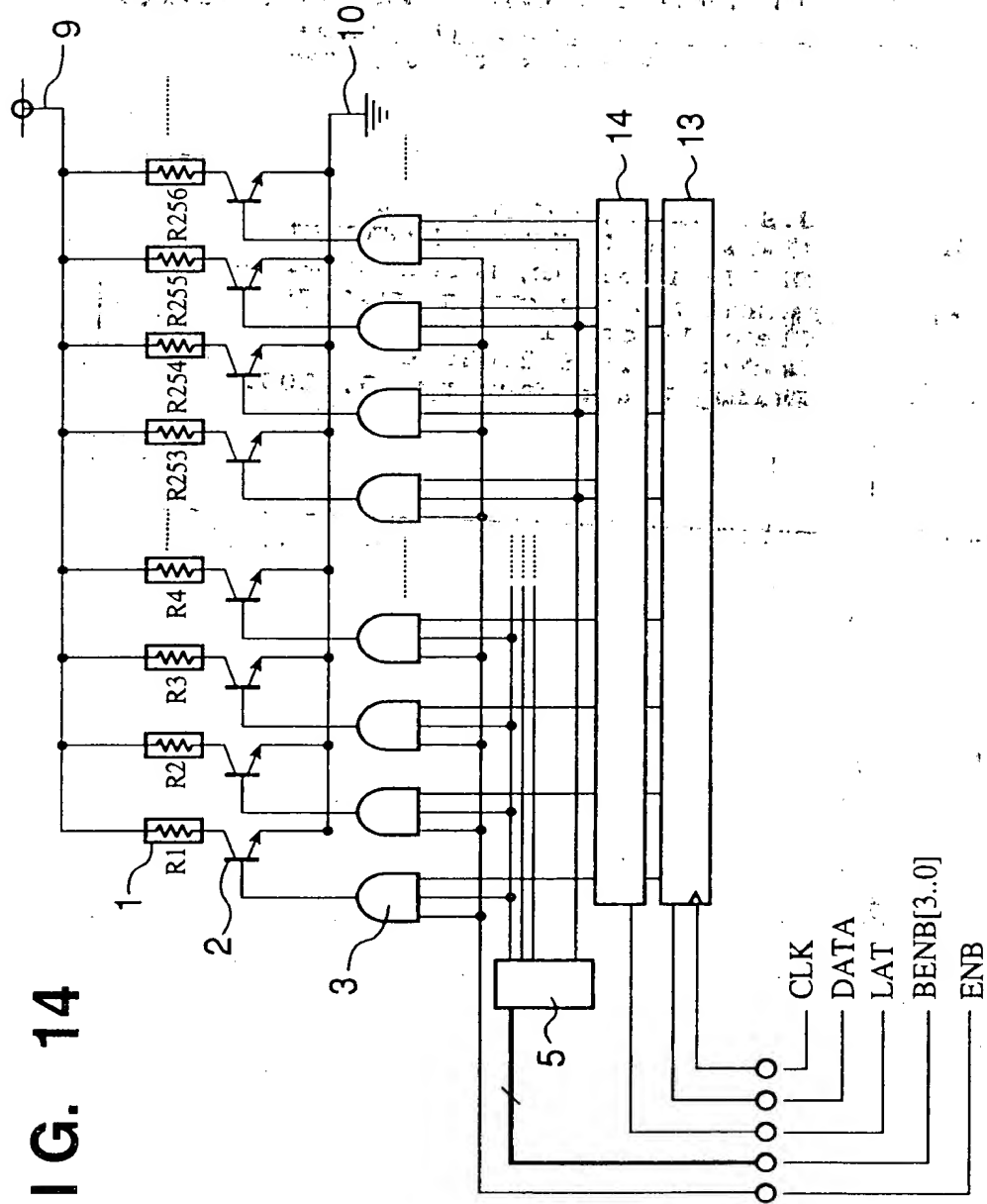


FIG. 15

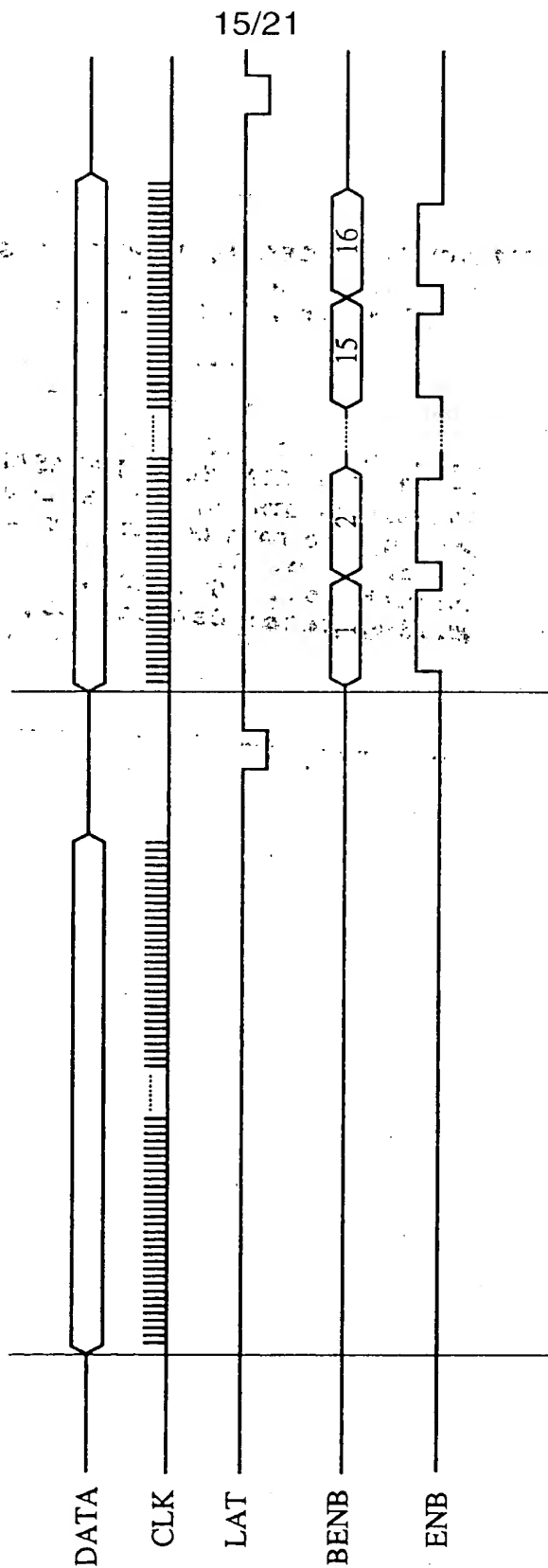


FIG. 16

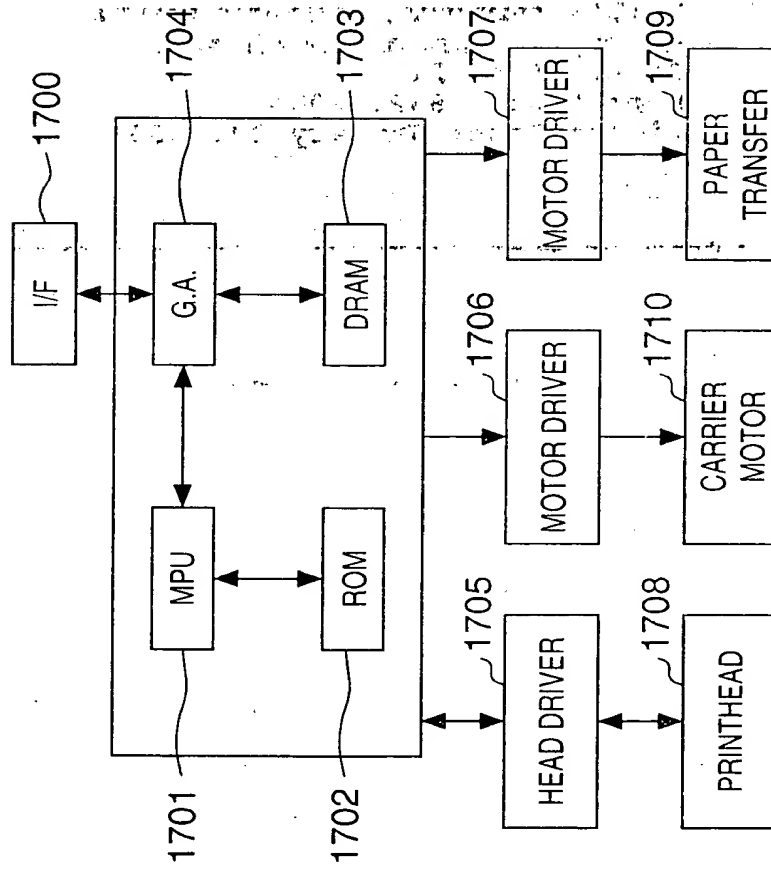


FIG. 17

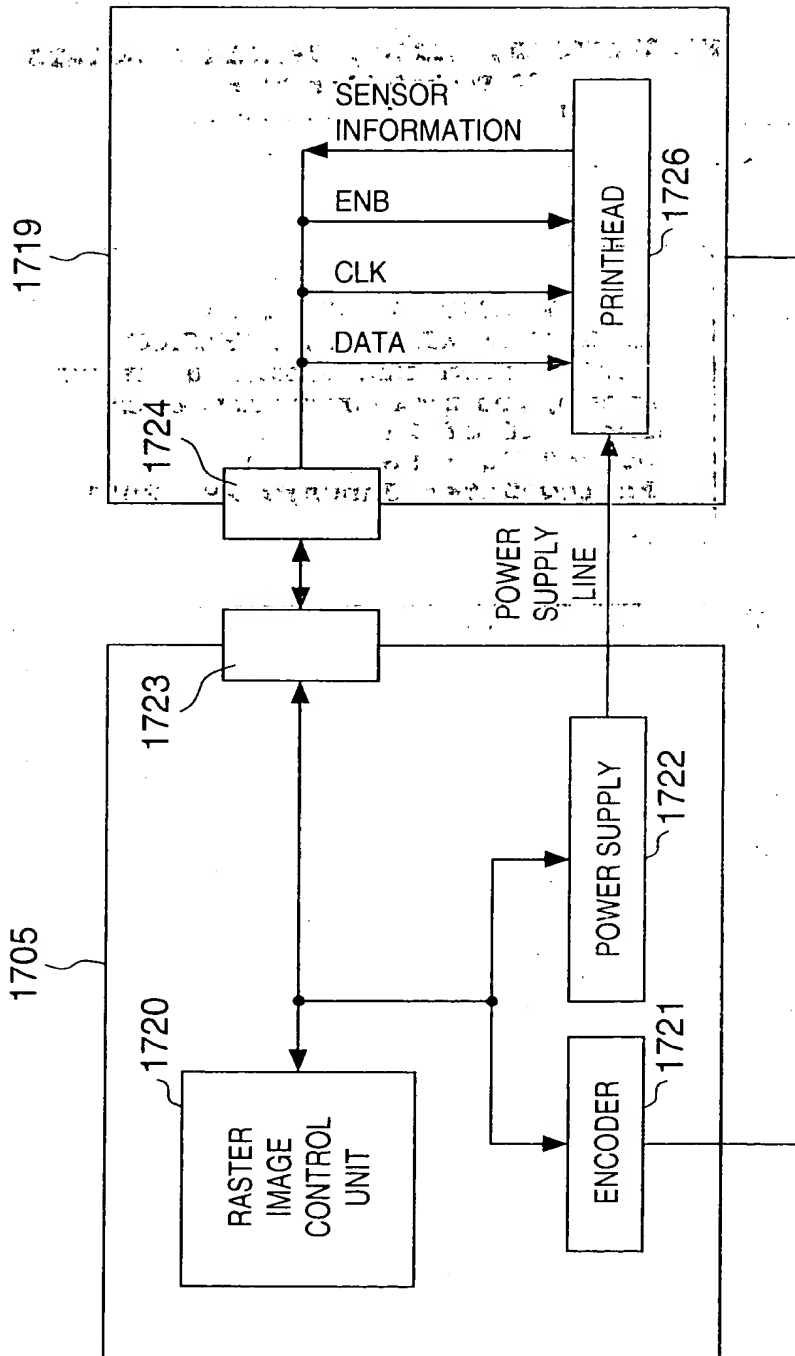


FIG. 18

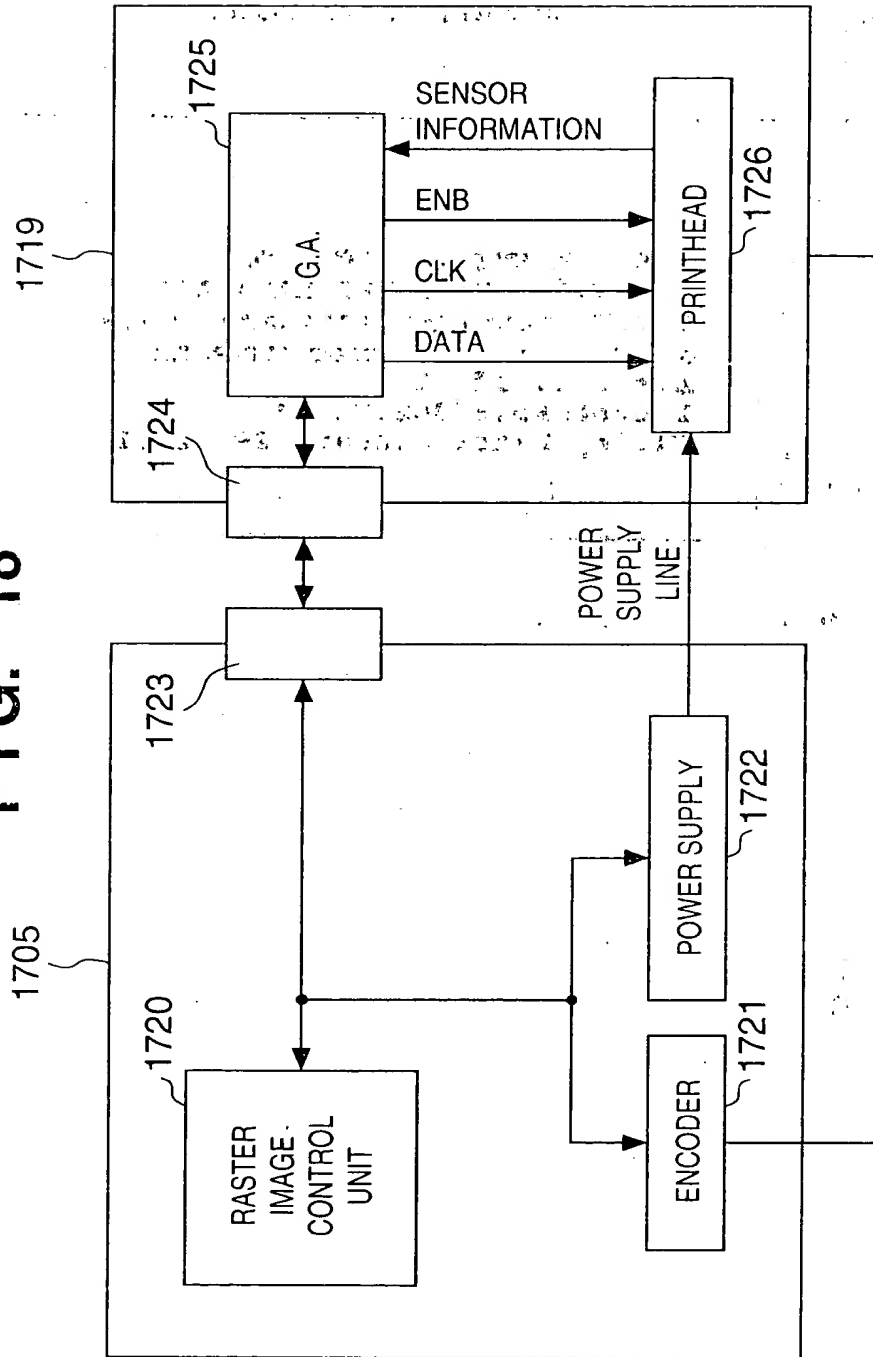


FIG. 19

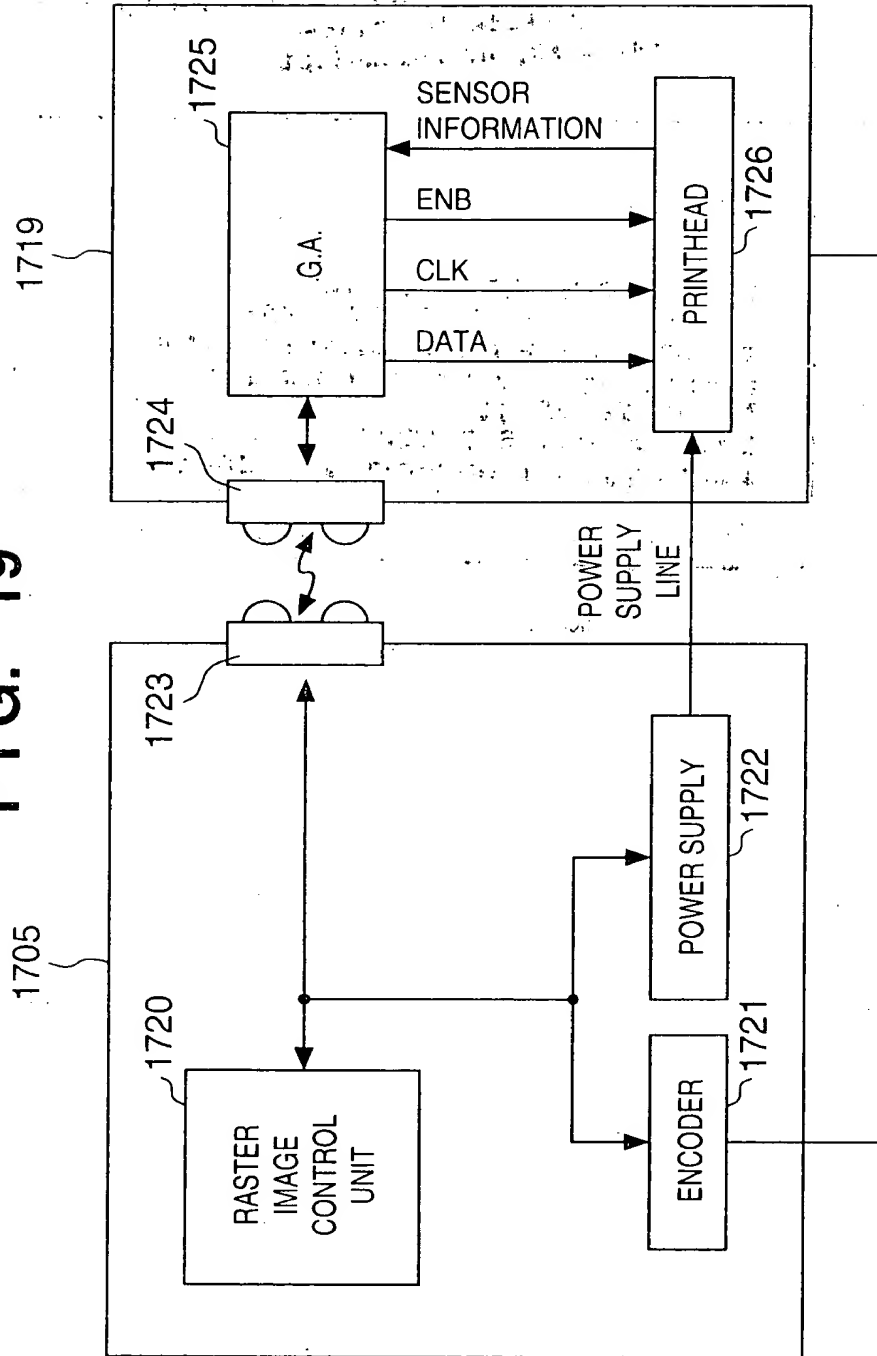


FIG. 20A

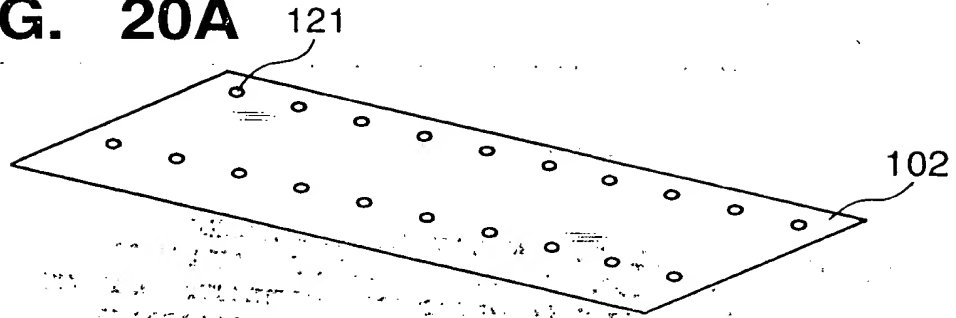


FIG. 20B

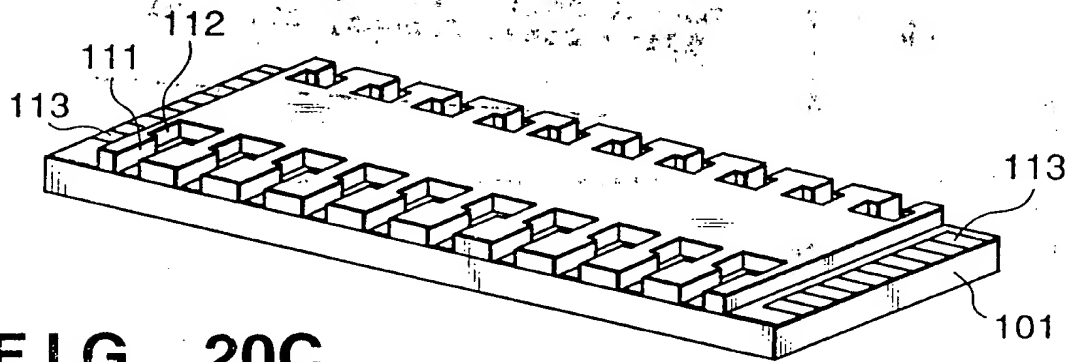


FIG. 20C

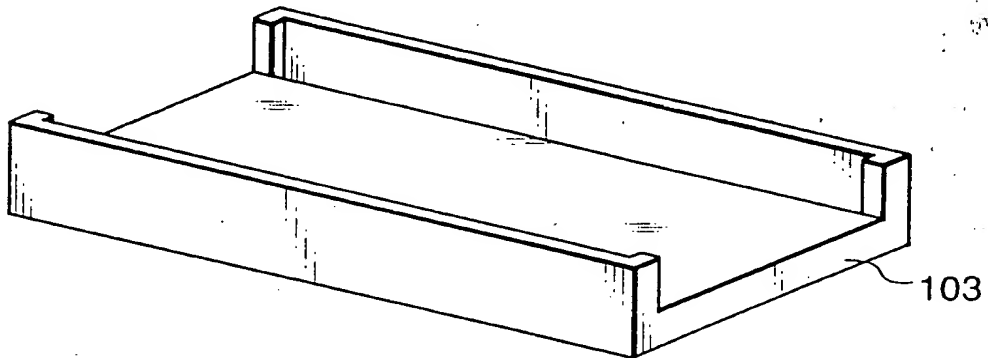


FIG. 20A

FIG. 21

